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[Title of the Invention] Liquid Crystal Display Device

[Claims]

[Claim 1] A liquid crystal display device

comprising a first transparent substrate which is provided with pixel electrodes and common electrodes substantially parallel and alternately arranged on a transparent insulating substrate, a plurality of pixels arranged in matrix form, scan lines and switching elements that individually control electric fields applied to pixel electrodes of said pixels, drain lines connected to said switching elements, common lines that supply a prescribed electric potential to common electrodes of said pixels and a first alignment layer formed on the highest layer;

a second transparent substrate arranged to confront said first transparent substrate and provided with at least a second alignment layer on the highest layer;

and a liquid crystal component layer sealed between said first transparent substrate and said second transparent substrate; characterized in that,

specific alignment processing is carried out such that alignment of said first alignment layer and said second alignment layer differs for regions of said drain lines and their vicinities (hereinbelow, referred to simply as

"drain line regions") and regions of apertures of said pixels and their vicinities (hereinbelow, referred to simply as "pixel aperture regions").

[Claim 2] A liquid crystal display device according to claim 1, characterized in that, said liquid crystal component has a positive dielectric constant anisotropy, and wherein the alignment process of said drain line regions is carried out in a direction substantially orthogonal to the longitudinal direction of said pixel electrodes.

[Claim 3] A liquid crystal display device according to claim 1, characterized in that, said liquid crystal component has a negative dielectric constant anisotropy, and wherein the alignment process of said drain line regions is carried out in a direction approximately parallel to the longitudinal direction of said pixel electrodes.

[Claim 4] A liquid crystal display device according to claim 1, characterized in that, said liquid crystal component has a negative dielectric constant anisotropy, and the alignment of said drain line regions is a vertical alignment.

[Claim 5] A liquid crystal display device according to any one of claim 1 to claim 4, characterized in that,

an alignment process is carried out such that the alignment of said pixel aperture region has an inclination of any angle θ that is neither orthogonal nor parallel to the longitudinal direction of said pixel electrode.

[Claim 6] A liquid crystal display device according to any one of claim 2 to claim 5, characterized in that, said first alignment layer and said second alignment layer are alignment layers that can be aligned by light, and are alignment layers processed by polarized light such that each of said drain line regions and said pixel aperture regions undergoes a stipulated alignment process.

[Claim 7] A liquid crystal display device according to any one of claim 1 to claim 6, characterized in that, the alignment process of regions other than said drain line regions and said pixel aperture regions is the same alignment process as the alignment process either of said drain line regions or of said pixel aperture regions.

[Claim 8] A liquid crystal display device comprising a first transparent substrate which is provided with pixel electrodes and common electrodes substantially parallel and alternately arranged on a transparent insulating substrate, a plurality of pixels

arranged in matrix form, scan lines and switching elements that individually control electric fields applied to pixel electrodes of said pixels, drain lines connected to said switching elements, common lines that supply a prescribed electric potential to common electrodes of said pixels and a first alignment layer formed on the highest layer;

a second transparent substrate arranged to confront said first transparent substrate and provided on the highest layer with at least a second alignment layer and a light-shielding layer having aperture regions of said pixels below said second alignment layer;

and a liquid crystal component layer sealed between said first transparent substrate and said second transparent substrate; characterized in that,

said liquid crystal component has a positive dielectric constant anisotropy, said first alignment layer and said second alignment layer undergo an alignment process so as to have an inclination of any angle θ which is neither parallel nor orthogonal to the longitudinal direction of said pixel electrode,

said light-shielding layer is formed from a conductor, and

voltage is applied to said light-shielding layer such that the director of liquid crystal molecules within said liquid crystal component layer in regions of said

drain lines and their vicinities is aligned substantially perpendicular to said first transparent substrate.

[Claim 9] A liquid crystal display device according to claim 8, characterized in that, voltage impressed to said light-shielding layer is either a direct-current voltage of a potential within a range of 10-20 V higher, or within a range of 10-20 V lower, than the average value of the potential of said drain lines, or an alternating voltage of a long period.

[Claim 10] A liquid crystal display device according to any one of claim 1 to claim 9, characterized in that, wherein said pixel electrodes and said drain lines extend over said common electrodes with an insulating layer, are parallel to said common electrodes, and separated from each other.

[Claim 11] A liquid crystal display device according to any one of claim 1 to claim 9, characterized in that, said common electrodes and said pixel electrodes extend parallel and separated from each other, and said drain lines extend parallel to said common electrodes with an interposed insulating layer.

[Detailed Description of Invention]**[0001]****[Technical field to which the invention belongs]**

The present invention relates to a horizontal-field-drive liquid crystal display, and particularly to a horizontal-field-drive liquid crystal display device that maintains a high aperture ratio while suppressing the occurrence of display defects such as vertical crosstalk and stepping non-uniformity.

[0002]**[Prior art]**

Liquid crystal displays of the prior art are typically of a type in which an image is displayed on a panel by causing an electric field to act in the direction perpendicular to the substrate surface, causing change in the alignment of the director (molecular axis) of the liquid crystal molecules, and thus controlling the transmittivity of light (hereinbelow, this type is referred to as "vertical-field-drive"), and Twisted Nematic (TN) mode is of this type. In liquid crystal displays of this vertical-field-drive type, the director is aligned perpendicular to the substrate surface when the field is being applied. As a result, the refractive index changes with the direction of viewing, thereby

strengthening the viewing angle dependency and making a wide viewing angle difficult to obtain.

[0003]

In contrast, recent years have seen research and development of horizontal-field-drive liquid crystal displays or IPS (In-Plane Switching) mode liquid crystal displays in which image display is realized through the control of the transmittivity of light by aligning the director of liquid crystal molecules parallel to the substrate surface, and causing an electric field to act in a direction parallel to the substrate surface to cause the director to rotate within a plane parallel to the substrate surface.

[0004]

As an example of a typical horizontal-field-drive liquid crystal display (hereinbelow referred to as simply "IPS liquid crystal display" or merely, "liquid crystal display"), the construction of an IPS liquid crystal display described in Japanese Patent Laid-open No. 36058/95 is next described with reference to Fig. 25 and Fig. 26. Fig. 25 is a plan view showing the structure of one pixel of the IPS liquid crystal display, and Fig. 26 is a sectional view showing the layer structure of the IPS liquid crystal display at line V-V of Fig. 25.

Prior-art IPS liquid crystal display 10 has a plurality of pixels arranged in matrix form, and as shown in Fig. 26, the display is provided with first transparent substrate (TFT substrate) 13, second transparent substrate (facing substrate) 15, and liquid crystal component layer 44 sealed between first transparent substrate 13 and second transparent substrate 15.

First transparent substrate 13 is provided with first glass substrate 12 on which electrodes (16, 20, 22) and switching structures (30, 32) are formed, and first alignment layer 28 is formed on the highest layer. Second transparent substrate (facing substrate) 15 is provided with second glass substrate 14, light-shielding layer 36, and second alignment layer 42, which are formed successively on second glass substrate 14. Second transparent substrate 15 is arranged such that second alignment layer 42 is parallel to and confronts first alignment layer 28 of first transparent substrate 13.

[0005]

Further, as shown in Fig. 25 and Fig. 26, each pixel comprises two common electrodes 16A and 16B extending over first glass substrate 12 separated from and parallel to each other, first insulating film 18 formed over common electrodes 16, drain line (signal line) 20 extending parallel to common electrodes 16 and is positioned over first insulating film 18 and between

common electrode 16B and common electrode 16A, and pixel electrode 22 similarly extends parallel to common electrodes 16 and is positioned over first insulating film 18 and between common electrodes 16B and common electrodes 16A of neighboring pixel. Common electrodes 16A and 16B are each connected to common line 24. First alignment layer 28 is stacked over drain line 20 and pixel electrode 22 with second insulating film 26 interposed. Pixel electrodes 22 and common electrodes 16 are actually alternately arranged so as to form a plurality of pairs.

The switching mechanism is made up of thin-film transistor 32 and scan line 30, which drives thin-film transistor 32, scan line 30 is connected to the gate electrode of thin-film transistor 32, drain line 20 is connected to the drain electrode, and pixel electrode 22 is connected to the source electrode.

[0006]

Black matrix 36 for shielding light which has an aperture region (hereinbelow referred to as aperture 34) on the pixel is formed on the surface of second glass substrate 14 that faces first transparent substrate 13, as shown in Fig. 26, and color filter 38 is formed over aperture 34 and over black matrix 36 around the periphery of aperture 34. Aperture 34 is opened in black matrix 36 as a rectangle demarcated by common line 24,

scan line 30, and two common electrodes 16, as shown in Fig. 25, and light advances through aperture 34 from the side of first transparent substrate 13 and toward the side of second transparent substrate 15 to realize image display.

Second alignment layer 42 which initial direction of orientation of the liquid crystal molecules of second alignment layer 42 is the same direction as that of first alignment layer 28 is formed on color filter 38 of second glass substrate 14 with planarization film 40 interposed.

Liquid crystal component layer 44 is accommodated and sealed between first alignment layer 26 and second alignment layer 42. As shown in Fig. 25, liquid crystal molecules are oriented by the aligning function of first alignment layer 26 and second alignment layer 28 such that their director forms any angle θ that is not orthogonal or parallel to the longitudinal direction of pixel electrode 22.

In addition, polarizing sheet (not shown) are provided on the outer sides of first glass substrate 12 and second glass substrate 14 as the polarizer of polarizing sheet (not shown) is parallel or orthogonal to the rubbing angle, analyzer of polarizing sheet (not shown) is orthogonal to the rubbing angle,

[0007]

The operation of above-described liquid crystal display 10 of the prior art is next described. Thin-film transistor 32 switches the electrode structure ON and OFF in accordance with ON/OFF signals received from scan line 30. When thin-film transistor 32 is ON, charge flows from drain line 20 to pixel electrode 22. When thin-film transistor 32 is OFF, pixel electrode 22 holds the charge and maintains a particular set potential. In contrast, a constant fixed direct-current voltage is applied to common electrodes 16.

A horizontal electric field is produced within liquid crystal component layer 44 in the direction parallel to first glass substrate 12 due to the difference in electric potential between pixel electrode 22 and common electrodes 16, and the liquid crystal molecules on pixel electrode 22 move. The potential difference between pixel electrode 22 and common electrodes 16 is held until writing of a signal of reversed polarity, and the display written to pixel electrode 22 is therefore displayed by way of aperture 34 of black matrix 36, by moving the liquid crystal molecules and controlling a permeability of light.

[0008]

The direction of the electric field of common electrodes 16 and the regions of the liquid crystal

component layer in the vicinity of common electrodes 16, and accordingly, the regions of the liquid crystal component layer alongside drain lines 20, is not parallel to first glass substrate 12, but rather, is close to perpendicular to glass substrate 12, so the optical transmittance therefore cannot be controlled as desired, the luminance is disturbed, and light that passes through this region must be blocked by black matrix 36.

The above-described IPS liquid crystal display of the prior art has the following problems.

First, these liquid crystal displays are prone to a problem called vertical crosstalk in which, when displaying a white window in a black display screen as shown in Fig. 27(a), the black display regions above and below the window of the display screen appear faintly white compared to other black display areas when the display screen is viewed from a side.

Second, there is the problem that, in the case of forming the electrodes of first and second transparent substrates by patterning by stepping projection alignment by a stepper, stepping non-uniformity is produced along the borders of the stepping projection alignment, as shown in Fig. 27(b).

[0009]

Regarding the first problem:

As described hereinabove, the proper control of light transmittivity is impossible, i.e., luminance cannot be controlled, in the regions adjacent to drain lines 20, i.e., the regions between drain line 20 and common electrodes 16 (the regions indicated by "a" in Fig. 28, and referred to hereinbelow as "region a"). Moreover, the luminance of these a regions (1) varies with fluctuation in the electric potential of the drain line with respect to the common electrodes and (2) varies with fluctuation in the distance between drain line and common electrodes.

The disadvantage of the vertical crosstalk shown in Fig. 27(a) arises from the change in electric potential of the drain lines with respect to common electrodes of (1). Signal potential is applied to the drain lines of pixels in which there is white display, and the potential in these drain lines is the average voltage of drain lines and therefore higher than average voltage of the drain lines of pixels in which there is no white display, so luminance thus varies under the influence of this difference in the portions above and below a window when viewed from an angle.

As can be understood from Fig. 28, region a is located exactly behind black matrix 36 and cannot be seen when a user views the display screen from directly in front of

the screen, and vertical crosstalk therefore does not occur in the display screen. In contrast, region a is visible when the user views the display screen from an angle, as shown in Fig. 28, and vertical crosstalk therefore occurs in the display screen.

[0010]

Regarding the second problem:

The cause of the second problem, the disadvantage of the stepping non-uniformity shown in Fig. 27(b) arises due to the variation in distance between drain lines and common electrodes of (2). The distance between drain lines and common electrodes varies due to aberration in the overlay of the layer in which common electrodes are formed and the layer in which drain lines and pixel electrodes are formed during stepping projection alignment by a stepper. This variation in distance results in variation in the luminance of region a, which in turn causes stepping non-uniformity when the screen is viewed from an angle.

[0011]

Two methods have been considered in the prior art for solving the above-described disadvantages of displays:

(1) Enlarging the dimensions of the black matrix such that region a cannot be seen even when the display screen is viewed from an angle; and

(2) overlaying the common electrodes and drain line such that region a does not exist.

However, these two methods have the following problems:

[0012]

First, as shown in Fig. 29(a), making the dimensions of black matrix 36 on the facing substrate-side equal to or greater than the distance between the outer edges of common electrodes 16A and 16B on the first transparent substrate side in the above-described method (1) hides region a from view even when the display screen is viewed from an angle.

In a case in which the black matrix extends beyond the common electrodes, however, the area of the aperture decreases as shown in Fig. 29(a) and the aperture ratio decreases. In addition, if a construction is adopted in which the outer edges of black matrix 36 are aligned with the outer edges of common electrodes 16, change or fluctuation in processing conditions causes a divergence in the positions of overlay of first transparent substrate and second transparent substrate, and this brings about a corresponding positional shift between black matrix 36 and common electrodes 16 as shown in Fig. 29(b), whereby the area of the aperture decreases and the aperture ratio falls.

[0013]

In the above-described method (2) in which the common electrodes and drain lines are overlapped so as to eliminate region a, parasitic capacitance 46 increases between common electrodes 16 and drain lines 20 as shown in Fig. 30(a). This capacitance effect weakens the signal waveform or increases transfer delay and prevents the accurate writing of potential to each pixel.

In addition, the presence of any holes or voids in interlayer insulation film 18 in the portions of overlap of common electrodes 16 and drain lines 20 as shown in Fig. 30(b) gives rise to shorts and the increased probability of a defective display device that is incapable of normal display.

[0014]

[Problems that the invention is to solve]

As described hereinabove, IPS liquid crystal displays of the prior art were not easily amenable to preventing display defects such as vertical crosstalk and stepping non-uniformity when the display screen is viewed at an angle from the side horizontally.

It is an object of the present invention to provide an IPS (In-Plane Switching) liquid crystal display that is free of display defects such as vertical crosstalk and stepping non-uniformity even when the display screen is viewed from the side horizontally, that maintains good

display uniformity and high aperture ratio, and that has high product yield during fabrication.

[0015]

[Means of solving the problems]

A liquid crystal display according to this invention (hereinbelow referred to as the first invention) is characterized to includes a first transparent substrate which is provided with pixel electrodes and common electrodes substantially parallel and alternately arranged on a transparent insulating substrate, a plurality of pixels arranged in matrix form, scan lines and switching elements that individually control electric fields applied to pixel electrodes of the pixels, drain lines connected to the switching elements, common lines that supply a prescribed electric potential to common electrodes of the pixels and a first alignment layer formed on the highest layer;

a second transparent substrate arranged to confront the first transparent substrate and provided with at least a second alignment layer on the highest layer;

and a liquid crystal component layer sealed between the first transparent substrate and the second transparent substrate; characterized in that,

specific alignment processing is carried out such that alignment of the first alignment layer and the second alignment layer differs for regions of the drain lines

and their vicinities (hereinbelow, referred to simply as "drain line regions") and regions of apertures of the pixels and their vicinities (hereinbelow, referred to simply as "pixel aperture regions").

[0016]

As a preferable embodiment of the first invention, the liquid crystal component has a positive dielectric constant anisotropy, and the alignment process of the drain line regions is carried out in a direction substantially orthogonal to the longitudinal direction of the pixel electrodes. Alternatively, the liquid crystal component has a negative dielectric constant anisotropy, and the alignment process of the drain line regions is carried out in a direction approximately parallel to the longitudinal direction of the pixel electrodes.

According to an embodiment of this invention, through the use of an specific alignment layer, the orientation of liquid crystal molecules in the drain line regions is substantially orthogonal to the longitudinal direction of the pixel electrodes, or is substantially parallel to the longitudinal direction of the pixel electrodes, accordingly, liquid crystal molecules are always in a stationary state with respect to an electric field in the direction orthogonal to the longitudinal direction

of the pixel electrodes regardless of the strength of the field. The liquid crystal molecules do not rotate and the transmittivity of light is therefore uniform. The luminance of regions between drain lines and common electrodes, which correspond to the drain line regions (region a in Fig. 28), is therefore a uniform luminance that is not dependent (1) on the rise and fall of the potential of the drain lines with respect to the common electrodes or (2) the amount of distance between the drain lines and common electrodes, and display defects such as vertical crosstalk or stepping non-uniformity therefore do not occur.

Further the angle θ can be any angle other than 0° and 90° , depending on the setting of optical characteristics of the liquid crystal display.

[0017]

According to another preferable embodiment of the first invention, the liquid crystal component layer has a negative dielectric constant anisotropy, and the orientation of the drain line region is oriented perpendicularly. Through the use of alignment layers which have specified in this embodiment, the initial orientation of the liquid crystal molecules in the drain line regions is in a direction orthogonal to the substrate, and as a result, liquid crystal molecules are

always in a stationary state with respect to an electric field in the direction orthogonal to the longitudinal direction of the pixel electrodes regardless of the strength of the electric field, therefor the liquid crystal molecules do not rotate and the transmittivity of light is therefore uniform. Accordingly, the luminance of the regions between the drain lines and common electrodes (region a in Fig. 28), which correspond to the drain line regions, is therefore a uniform luminance that is not dependent (1) on the rise and fall of the electric potential of the drain lines (signal electrode) with respect to the common electrodes or (2) the amount of distance between the drain lines and common electrodes, and display defects such as vertical crosstalk or stepping non-uniformity therefore do not occur.

Further the angle θ can be any angle other than 0° and 90° , depending on the setting of optical characteristics of the liquid crystal display.

[0018]

In the first invention, alignment processing is carried out such that the orientation of the pixel aperture regions has an inclination of any angle θ that is neither parallel nor orthogonal to the longitudinal direction of the pixel electrodes.

The alignment processing of regions other than the drain line regions and pixel aperture regions is the same as the alignment processing of the drain line regions or pixel aperture regions.

[0019]

Any method of alignment processing of the first alignment layer and second alignment layer may be used in the first invention. For example, the first alignment layer and the second alignment layer may be alignment layers that have undergone rubbing such that the alignment layer of the alignment layer in the drain line regions and pixel aperture regions undergo the stipulated alignment process; or may be alignment layers capable of light orientation that have undergone a polarized light irradiation process such that the first designated regions and the second designated regions and pixel aperture regions undergo the stipulated alignment process.

Therefore the first alignment layer and the second alignment layer are not limited to the alignment layer undergo the stipulated alignment process, are alignment layers capable of light orientation, and are alignment layers that have undergone the stipulated alignment process such that the alignment layer in the drain line regions and pixel aperture regions undergo the stipulated alignment process

[0020]

A liquid crystal display according to this invention (hereinbelow referred to as the second invention) is characterized to includes a first transparent substrate which is provided with pixel electrodes and common electrodes substantially parallel and alternately arranged on a transparent insulating substrate, a plurality of pixels arranged in matrix form, scan lines and switching elements that individually control electric fields applied to pixel electrodes of the pixels, drain lines connected to the switching elements, common lines that supply a prescribed electric potential to common electrodes of the pixels and a first alignment layer formed on the highest layer;

a second transparent substrate arranged to confront the first transparent substrate and provided on the highest layer with at least a second alignment layer and a light-shielding layer having aperture regions of the pixels below the second alignment layer;

and a liquid crystal component layer sealed between the first transparent substrate and the second transparent substrate; characterized in that,

the liquid crystal component has a positive dielectric constant anisotropy, the first alignment layer and the second alignment layer undergo an alignment process so as to have an inclination of any angle θ which is

neither parallel nor orthogonal to the longitudinal direction of the pixel electrode, the light-shielding layer is formed from a conductor, and

voltage is applied to the light-shielding layer such that the director of liquid crystal molecules within the liquid crystal component layer in regions of the drain lines and their vicinities is aligned substantially perpendicular to the first transparent substrate.

[0021]

In a preferable embodiment of the second invention, the voltage impressed to the light-shielding layer is a direct-current voltage having a potential within a range 10-20 V higher than, or within a range of 10-20 V lower than, the average value of the potential of the drain lines, or is an alternating voltage having a long period.

In the second invention, the director of the liquid crystal molecules is aligned substantially perpendicular to the first transparent substrate in the region (region a in Fig. 28) between the drain lines and common electrodes, which corresponds to the drain line region, and the transmittivity of light is therefore uniform regardless of the strength of the electric field between the common electrodes and pixel electrodes. As a result, the luminance of the drain line regions is uniform and

not dependent (1) on the rise and fall of electric potential of the drain lines with respect to the common electrodes or (2) the amount of distance between the drain lines and the common electrodes, and display defects such as vertical crosstalk or stepping non-uniformity do not occur.

The angle θ depends on the setting of optical characteristics of the liquid crystal display and can be any angle other than 0° and 90° .

[0022]

In the first invention and second invention, the arrangement of such components as the pixel electrodes, drain lines, and common electrodes is open to variation, and for example, the pixel electrodes and drain lines may both be parallel to the common electrodes with an interposed insulating layer and extend at a distance from each other. In addition, the common electrodes and pixel electrodes may extend parallel and at a distance from each other, and the drain lines may extend parallel to the common electrodes with an interposed insulating layer.

[0023]

[Mode for carrying out of the invention]

Embodiments of the present invention are next explained concretely and in detail with reference to the figures.

and embodiments.

The first embodiment of the first invention:

This embodiment is an example of the liquid crystal display device of the first invention. Fig. 1 is a plan view showing the construction of one pixel in the liquid crystal display according to this embodiment, Fig. 2 is a sectional view at line I-I of Fig. 1, Fig. 3 is a schematic view showing the partition of first alignment layer and second alignment layer and Fig. 4 is a schematic view showing the initial direction of alignment of liquid crystal molecules of an alignment layer in each partition shown in Fig. 3.

Liquid crystal display 50 according to this embodiment is a liquid crystal display generally described as the horizontal-field-drive type (hereinbelow referred to simply as "IPS liquid crystal display" or merely, "liquid crystal display"). This embodiment has basically the same structure as IPS liquid crystal display 10 described as the prior art with the exception of the rubbing angle of first alignment layer 51 and second alignment layer 53, and accordingly, the initial direction of orientation of the liquid crystal molecules within the liquid crystal component layer.

[0024]

Liquid crystal display 50 includes a plurality of

pixels arranged in a matrix form and is provided with first transparent substrate (TFT substrate) 13, second transparent substrate (facing substrate) 15, and liquid crystal component layer 44 sealed between first transparent substrate 13 and second transparent substrate 15, as shown in Fig. 2.

First transparent substrate 13 is provided with first glass substrate 12 that includes electrodes (16, 20, 22) and switching structures (30, 32), and first alignment layer 51. Second transparent substrate (facing substrate) 15 is provided with light-shielding layer 36 and second alignment layer 53 formed in succession on second glass substrate 14, and is arranged such that second alignment layer 53 is parallel to and confronting first alignment layer 51 of first transparent substrate 13.

[0025]

As shown in Fig. 1, each pixel are made up of two common electrodes 16A and 16B that extend parallel and separated from each other on first glass substrate 12, first insulating film 18 formed over common electrodes 16, drain line (signal electrode) 20 that extends parallel to common electrodes 16 and arranged over first insulating film 18 between common electrode 16A and common electrode 16B, and pixel electrode 22 that similarly extends parallel to common electrodes 16 and

arranged over first insulating film 18 and between common electrode 16A and common electrode 16B. Common electrode 16A and common electrode 16B are each connected to common line 24. Pixel electrode 22 and common electrodes 16 are actually arranged to form a plurality of alternately arranged pairs.

Switching mechanism (30, 32) is made up of thin-film transistor 32 and scan line 30 for driving thin-film transistor 32, scan line 30 being connected to the gate electrode of thin-film transistor 32, drain line 20 being connected to the drain electrode, and pixel electrode 22 being connected to the source electrode.

First alignment layer 51 is stacked on first glass substrate 12 over drain lines 20 and pixel electrodes 22 with an interposed second insulating film 26.

[0026]

Black matrix 36 for blocking light and including pixel aperture region 34 (hereinbelow referred to as "aperture 34") and color filter 38 formed over aperture 34 and over black matrix 36 around the periphery of aperture 34 are formed on the facing surface of second glass substrate 14, as shown in Fig. 2. Aperture 34 is opened in black matrix 36 as a rectangle demarcated by common line 24, scan line 30, and two common electrodes 16, as shown in Fig. 1, and light passes through aperture 34

from first transparent substrate 13 toward second transparent substrate 15 to realize image display.

Liquid crystal component layer 44 is accommodated and sealed between first alignment layer 51 and second alignment layer 53.

[0027]

In this embodiment, first alignment layer 51 and second alignment layer 53 are partitioned into strip region 52 that includes the region corresponding to aperture 34, and region 54A and region 54B at the two sides of region 52, as shown in Fig. 3, and Region 52 and region 54A and 54B on both sides of region 52 are characterized by having different directions of initial orientation of the liquid crystal molecules in the alignment layer.

Region 52 is made up of three regions, region 55 corresponding to the aperture 34 shown in Fig. 1, and regions 55A and 55B that correspond to extensions 34A and 34B, respectively, that extend outward in the longitudinal direction from both ends of aperture 34.

Region 55 of region 52 accordingly lies in first alignment layer 51 and second alignment layer 53 below aperture 34 of black matrix 36, region 55A and region 55B of region 52 as well as region 54A and region 54B on both sides of region 52 all lie below the regions of black matrix 36 apart from aperture 34. The drain line regions (region a in Fig. 28) are included within region

54A and region 54B.

[0028]

First alignment layer 51 and second alignment layer 53 are alignment layers that have been processed such that, the direction of initial orientation of liquid crystal molecules in region 52 have an inclination of θ° with respect to the longitudinal direction of pixel electrode 22 within a plane that is parallel to first glass substrate 12, as shown in Fig. 4. In other words, the rubbing angle of the alignment layer of region 52 is θ , and the direction of initial orientation of liquid crystal molecules has an inclination of θ° with respect to the longitudinal direction of pixel electrode 22 within a plane that is parallel to first glass substrate 12. The angle θ can be any angle other than 0° and 90° depending on the setting of optical characteristics of the liquid crystal display.

[0029]

In region 54A and region 54B on both sides of region 52, on the other hand, first alignment layer 51 and second alignment layer 53 are alignment layers that have been processed such that the direction of initial orientation of the liquid crystal molecules is orthogonal to the longitudinal direction of pixel

electrode 22 within a plane that is parallel to first glass substrate 12 when the dielectric constant anisotropy $\Delta\epsilon$ of the liquid crystal molecules of liquid crystal component layer 44 is greater than 0 as shown in Fig. 4, and such that the direction of initial orientation of liquid crystal molecules is parallel to the longitudinal direction of pixel electrode 22 within a plane that is horizontal when the dielectric constant anisotropy $\Delta\epsilon$ of the liquid crystal molecules is less than 0.

Therefore, the direction of initial orientation of the liquid crystal molecules of liquid crystal component layer 44 is orthogonal to the longitudinal direction of pixel electrode 22 when dielectric constant anisotropy $\Delta\epsilon$ of the liquid crystal molecules is greater than 0, and the direction of initial orientation of the liquid crystal molecules of liquid crystal component layer 44 is parallel to the longitudinal direction of pixel electrode 22 when the dielectric constant anisotropy $\Delta\epsilon$ of the liquid crystal molecules is less than 0.

In addition, the electric potential of black matrix 36 is preferably floating when the dielectric constant anisotropy of the liquid crystal molecules is positive, while any potential can be given when the dielectric

constant anisotropy is negative.

[0030]

The action of liquid crystal display 50 of this embodiment is next described with reference to Fig. 5 for a case in which the dielectric constant anisotropy $\Delta\epsilon$ of the liquid crystal molecules is greater than 0. In a region that includes region 54A and region 54B, i.e., regions alongside the drain line (region a in Fig. 28), the direction of initial orientation of the liquid crystal molecules is perpendicular to pixel electrodes 22, and accordingly, to drain lines 20, and as a result, remain constantly stationary regardless of the strength of the electric field in the direction orthogonal to drain line 20, and the transmittivity of light is therefore uniform. In region 52, on the other hand, the liquid crystal molecules rotate according to the strength of the electric field, and the transmittivity of light is controlled.

Fig. 6 is a graph showing the relation between the electric field strength between common electrodes 16 and pixel electrode 22 and the optical transmittivity (%) of liquid crystal display 50 when the quantity of light emitted from a polarizing sheet on first glass substrate 12 is 100. In region 52, the optical transmittivity of the liquid crystal increases as the strength of the

electric field increases. In region 54A and region 54B, on the other hand, the liquid crystal does not move despite increased strength of the electric field, and the optical transmittivity is therefore uniform. The luminance of regions along the drain lines is therefore uniform regardless of the rise and fall of potential of the drain lines with respect to the common electrodes or amount of space between drain lines and common electrodes.

As a result, display defects such as vertical crosstalk and stepping non-uniformity that were present when observing the display surface of a prior-art IPS liquid crystal display from an angle are not present in liquid crystal display 50 of this embodiment, and the display quality is improved.

[0031]

The method of fabricating liquid crystal display 50 (the first):

A method of fabricating liquid crystal display 50 (the first) having liquid crystal molecules in which dielectric constant anisotropy $\Delta\epsilon$ is greater than 0 is next described with reference to Figs. 7. Fig. 7(a)-Fig. 7(c) is a schematic perspective view showing the each step in the process of fabricating a liquid crystal display 50.

(1) First, alignment layers having a pretilt angle of 5° or less are used as first alignment layer 51 and second alignment layer 53. Next, as shown in Fig. 7(a), the entire surface of the alignment layers, i.e., all of region 52, region 54A and region 54B, undergo rubbing so as to acquire the direction of initial orientation θ of region 52. The angle θ can be any angle other than 0° and 90° , depending on the set optical characteristics of the liquid crystal display. This is also true of the second embodiment and the third embodiment described hereinbelow.

(2) Next, a photoresist film is applied to the entire surface of the alignment layers, following which patterning is carried out by photolithography and wet etching to leave photoresist film 56 on only region 52 and thereby mask region 52. Next, as shown in Fig. 7(b), the alignment layers of region 54A and region 54B undergo a rubbing process such that the direction of orientation of the liquid crystal molecules of region 54A and 54B is orthogonal to the longitudinal direction of pixel electrode 22. The photoresist film may be either posi-resist or nega-resist, and as an example, OFPR-800C of Tokyo Applications (KK) may be used as the photoresist film and trimethylamine may be used as the developer.

(3) Finally, as shown in Fig. 7(c), the desired first alignment layer 51 and second alignment layer 53 can be obtained by stripping off photoresist film 56 on region 52 using a stripper, and then washing in water at room temperature. ethyl lactate, dimethylsulfoxide, or mono ethanolamine may be used as the resist stripper.

[0032]

The method of fabricating liquid crystal display 50 (the second):

A method of fabricating liquid crystal display 50 (the second) is next described with reference to Figs. 8. Fig. 8(a)- Fig. 8(c) is a schematic perspective view showing the each step in the process of fabricating a liquid crystal display 50.

(1) In this method, alignment layers that can align light are used as first alignment layer 51 and second alignment layer 53. As an alignment layer that can align light, an alignment layer may be used that is composed of a polymer such as a photosensitive radical polymerized by a polarized light irradiation described in the AM-LCD '96/IDW '96 Digest of Technical Papers (p. 337).

An alignment layer is first applied to substrate , following which regions 54A and 54B are covered by a mask 58 as shown in Fig. 8(a), and region 52 is irradiated such that the direction of initial

orientation of the liquid crystal molecules of region 52 is in the direction by polarized light having an angle θ with respect to the longitudinal direction of common electrodes 16. The direction of initial orientation of liquid crystal molecules in region 52 is thus θ° with respect to the longitudinal direction of common electrodes 16.

(2) Next, region 52 is covered by another mask 60, and region 54A and region 54B are irradiated by polarized light such that the direction of initial orientation of the liquid crystal molecules of region 54A and region 54B is in the direction orthogonal to the longitudinal direction of pixel electrode 22, and accordingly, of drain line 20, thereby obtaining the desired first alignment layer 51 and second alignment layer 53.

[0033]

The first embodiment of the first invention:

This embodiment is an another example of the liquid crystal display device of the first invention. Fig. 9 is a plan view showing the construction of one pixel in the liquid crystal display according to this embodiment, Fig. 10 is a sectional view at line II-II of Fig. 9.

In liquid crystal display 60 according to this embodiment, the structure of first alignment layer 62 and second alignment layer 64 are changed, the direction

of initial orientation of liquid crystal molecules within the liquid crystal component layer is accordingly different, and the dielectric constant anisotropy $\Delta\epsilon$ of liquid crystal molecules is limited to a negative value. With the exception of these differences, this embodiment, like the first embodiment, is basically an IPS liquid crystal display having an architecture similar to liquid crystal display 10 described as the prior-art example.

[0034]

As shown in Fig. 10, first alignment layer 62 is stacked over drain lines 20 and pixel electrodes 22 on the side of first glass substrate 12, with second insulating film 26 interposed. As first alignment layer 62, horizontal alignment layer 66 is used in region 52, and vertical alignment layer 68 is used in region 54A and region 54B, i.e., in the regions including the region along the drain lines 20 below black matrix 36 (region a in Fig. 28).

Second alignment layer 64, on the other hand, is formed on the facing surface of second glass substrate 14 with interposed planarization film 40. As with first alignment layer 62, horizontal alignment layer 66 is used as second alignment layer 64 in region 52, and vertical alignment layer 68 is used as second alignment

layer 64 in region 54A and region 54B, i.e., the regions that include the regions alongside drain line 20 below black matrix 36 (region a in Fig. 28). Vertical alignment layer 68 is an alignment layer having the function of orienting liquid crystal molecules within liquid crystal component layer 44 in a direction orthogonal to alignment layer 68.

A liquid crystal is used in which the dielectric constant anisotropy $\Delta\epsilon$ of the liquid crystal molecules is negative.

Horizontal alignment layer 66 is an alignment layer treated such that the direction of initial orientation of the director of liquid crystal molecules has an inclination of angle θ with respect to the longitudinal direction of pixel electrodes 22 in a horizontal plane, or in other words, has a rubbing angle of θ .

Liquid crystal component layer 44 is accommodated between first alignment layer 26 and second alignment layer 42.

[0035]

In this embodiment, therefore, the direction of initial orientation of the liquid crystal molecules of liquid crystal component layer 44 has an inclination of θ with respect to the longitudinal direction of pixel electrode

22 and is oriented parallel to first glass substrate 12 and second glass substrate 14 in region 52, and is oriented orthogonal to region 54A and region 54B, as shown in Fig. 9 and Fig. 10.

In addition, the electric potential of black matrix 36 is floating.

[0036]

In liquid crystal display 60 of this embodiment, vertical alignment layer 68 is provided as the alignment layer in region 54A and region 54B, and the liquid crystal molecules are therefore oriented vertically as shown in Fig. 11. The movement of liquid crystal molecules is therefore suppressed by the normal electric field strength of the liquid crystal display, and because light is unaffected by double refraction, light is not transmitted in region 54A and region 54B. In region 52 where horizontal alignment layer 66 is provided as the alignment layer, in contrast, the liquid crystal molecules rotate according to the strength of the electric field and the transmittivity of light therefore varies with the electric field strength.

As shown in Fig. 12 and similar to Fig. 6, transmittivity changes with field strength in region 52 and thus operates normally as a display device. In region 54A and region 54B, in contrast, almost no light is transmitted despite changes in the degree of the

electric field. In other words, the luminance in regions alongside the drain lines (region a in Fig. 28) is fixed (nearly black) regardless of the drain line potential or the amount of space between the drain lines and common electrodes. Due to these factors, the vertical crosstalk or stepping non-uniformity that were visible when viewed from an angle in liquid crystal display 10 of the prior art are not seen in this liquid crystal display 60, and display quality is improved.

[0037]

The method of fabricating liquid crystal display 60 (the first):

A method of fabricating liquid crystal display 60 (the first) is next described with reference to Figs. 13. Fig. 13(a)- Fig. 13(d) is a schematic perspective view showing the each step in the process of fabricating a liquid crystal display 60.

In this method, a vertical alignment layer in which liquid crystal molecules are oriented vertically when no voltage is applied is used in the alignment layers of region 54A and region 54B, and a horizontal alignment layer is used in the alignment layer of region 52, as in the prior art. The liquid crystal material employed has a negative dielectric constant anisotropy.

(1) First, as shown in Fig. 13(a), horizontal alignment layer 66 is applied to the entire surface of substrate

65 and then undergoes rubbing at a prescribed rubbing angle θ . Next, photoresist film 70 is applied, and region 52 is then exposed to light through photo-mask 74 having pattern 72.

(2) The photoresist and horizontal alignment layer 66 of regions other than region 52 is then removed by wet etching using an alkali liquid. Photoresist film 70 of region 52 is then stripped off, and as shown in Fig. 13(b), a patterned horizontal alignment layer 66 is formed on region 52.

(3) Next, vertical alignment layer 68 is applied to the entire substrate surface as shown in Fig. 13(c). Photoresist film 75 is applied, and region 54A and region 54B are exposed to light using photo-mask 77 having pattern 76. As the material for vertical alignment layer 68, for example, Nissan Chemicals (KK) product SE1211 may be used.

(4) The photoresist and vertical alignment layer 68 are next patterned by etching as shown in Fig. 13(d) to obtain the desired first alignment layer 62 and second alignment layer 64.

[0038]

In cases in which no problem is presented by a difference in level of the alignment layer between region 52 and region 54A and 54B alongside the drain

line, the patterning of step (2) may be omitted.

The order of formation of horizontal alignment layer 66 and vertical alignment layer 68 may be the reverse of this embodiment, vertical alignment layer 68 first being applied and patterned, and then horizontal alignment layer 66 being applied, rubbed, and patterned. In addition, an optical orienting method may be used to establish initial orientation of horizontal alignment layer 66 rather than a rubbing method.

[0039]

The method of fabricating liquid crystal display 60 (the second):

A method of fabricating liquid crystal display 60 (the second) is next described with reference to Figs. 14.

Fig. 14(a)- Fig. 13(c) is a schematic perspective view showing the each step in the process of fabricating a liquid crystal display 60.

(1) First, horizontal alignment layer 66 is applied to the entire surface of substrate 65, as shown in Fig. 14(a).

(2) Next, vertical alignment layer 68 is formed on portions of horizontal alignment layer 66 of region 54A and region 54B using printing plate 78 having a prescribed aperture pattern, as shown in Fig. 14(b)

(3) Horizontal alignment layer 66 next undergoes rubbing at a prescribed rubbing angle, and in this way, desired

first alignment layer 62 and second alignment layer 64 can be obtained as shown in Fig. 14(c).

[0040]

If a difference in level on the alignment layer between region 54A and region 54B alongside drain lines and region 52 presents a problem, the pattern formation of horizontal alignment layer 66 is preferably carried out by printing plate only on region 52.

The order of formation of horizontal alignment layer 66 and vertical alignment layer 68 may also be the reverse of the order in this embodiment, and the initial orientation of horizontal alignment layer 66 may be established by an optical orientation method rather than by a rubbing method.

[0041]

In the first embodiment and the second embodiment of the first invention, the alignment process of first alignment layer 51 and 62 and second alignment layer 53 and 64 was carried out by partitioning between region 52, region 54A, and region 54B. However, region 52 of first alignment layer 51 and 62 and second alignment layer 53 and 64 may be partitioned into region 55, region 55A, and region 55B (see Fig. 3), the same alignment process then carried out for region 55 as was carried out for region 52 in the first embodiment and the second embodiment, and the same alignment process

carried out for region 55A, 55B, 54A and 54B as was carried out for region 54A and region 54B in the first embodiment and second embodiment.

[0042]

The embodiment of the second invention:

This embodiment is an example of the liquid crystal display device of the second invention. Fig. 13 is a plan view showing the construction of one pixel in the liquid crystal display according to this embodiment, Fig. 16 is a sectional view at line III-III of Fig. 13, Fig. 17 is an overall schematic view of a pixel unit, Fig. 18 is a partial sectional view at line IV-IV of Fig. 17;

With the exception of a difference in the structure of black matrix 82, liquid crystal display 80 according to this embodiment is a IPS liquid crystal which have basically the same construction as IPS liquid crystal display 10 of the prior art. In addition, the liquid crystal used in this embodiment has a positive dielectric constant anisotropy.

[0043]

In this embodiment, black matrix 82 is made of a conductor such as metal, and either a direct-current voltage having a potential 10-20 V higher, or 10-20 V lower, than the average value of the signal potential of

drain line 20, or an alternating voltage that reverses between positive and negative, is applied to black matrix 82. The signal potential of drain lines 20 is 0 V during black display and 6.5 V during white display, the potential of pixel electrodes 22 is 0 V during black display and 6.5 V during white display, and the potential of common electrodes 16 is always 0 V.

A strong field perpendicular to first glass substrate 12 and second glass substrate 14 is thus generated in the region around drain line 20. The direction of the director of the liquid crystal is therefore always oriented in a direction substantially perpendicular to the substrate.

In this embodiment, moreover, the rubbing angle of first alignment layer 28 on the side of first transparent substrate 13 (see Fig. 26) and second alignment layer 42 of second transparent substrate 15 (see Fig. 26) is the same as in the prior art, and can be any angle greater than 0° and less than 90° .

[0044]

The construction of the means for applying voltage to black matrix 82 is next described with reference to Fig. 17 and Fig. 18.

The end of black matrix 82 is connected to terminal 86 of first glass substrate (TFT substrate) 12 by way of a conductive material, for example, silver paste 84. The

interior, including liquid crystal component layer 44, is sealed by seal 90. In addition, the part identified as "92" in Fig. 18 is a color filter substrate, which is the second substrate.

In concrete terms, silver paste installation points of TFT substrate 12 and terminal 86 are first linked by a metal pattern (not shown). TCP 88 is next affixed to terminal 86, and a direct-current voltage 10-20 V higher, or 10-20 V lower, than the average value of the potential of a drain signal, or an AC voltage having a long period of, for example, one positive/negative reversal per hour, is applied to TCP 88.

[0045]

The action of liquid crystal display 80 according to a third embodiment is next explained with reference to Fig. 19 and Fig. 20. Fig. 19 and Fig. 20 show the calculated potential during black display and during white display in region 54A and 54B, i.e., the liquid crystal component layer below black matrix 82. A potential of -10 V is set to black matrix 82, and potentials of 0 V during black and 6.5 V during white are set to the drain lines.

In a liquid crystal material for which $\Delta\epsilon$ is greater than 0 and the field direction is a direction

perpendicular to an equipotential line, the longitudinal direction of the liquid crystal molecules tends to align in the direction of the field. Compared with the prior-art example shown in Fig. 21, a strong field is generated in a vertical direction alongside the drain line in Fig. 19 and Fig. 20, so the liquid crystal molecules therefore stand vertical and are therefore not as prone as the prior art example to influence from the horizontal transverse field produced between the drain lines and common electrodes. It can also be seen from Fig. 19 and Fig. 20 that the potential of the black matrix does not disturb the horizontal transverse field in the aperture region.

Skew of the equipotential line over electrodes is caused by disclination of the liquid crystal molecules.

In the calculation of potential in the prior art, the black matrix was treated as nonmetallic for the sake of expediency.

[0046]

As shown in Fig. 22 and similar to Fig. 6, the above-described construction results in luminance of the region alongside drain lines (region a in Fig. 29(a)) that is uniform (nearly black) regardless of the rise and fall in the potential of the drain lines or the amount of space between the drain lines and common electrodes, in contrast with the prior-art example shown

in Fig. 23. As a result, the vertical crosstalk and stepping non-uniformity present in the prior art when viewing from an angle are not present in this liquid crystal display 80, and the display quality is improved.

[0047]

An another embodiment (the first):

Although pixel electrodes 22 extend parallel to drain lines 20 over common electrodes 16 with an interposed insulating film 18 as shown in Fig. 2 in the first embodiment and second embodiment of the first invention as well as in the embodiment of the second invention, the invention is not limited to this configuration, and for example, pixel electrodes 22 may extend over first glass substrate 12 arranged parallel with common electrodes 16A and 16B as shown in Fig. 24.

[0048]

An another embodiment (the second):

Although first alignment layer and second alignment layer were partitioned into region 52, region 54A, and region 54B in the first embodiment and second embodiment of the first invention, the invention is not limited to this form, and the first alignment layer and the second alignment layer may be partitioned into region 55 and regions other than region 55, the alignment process applied to region 52 may be applied to region 55, and the alignment process applied to region 54A and region

54B may be applied to the regions other than region 55. In addition, partitioning between the drain line region and regions other than the drain line region is also possible, and the alignment process applied to region 54A and region 54B may be applied to the drain line region, and the alignment process applied to region 52 may be applied to the regions other than the drain line region.

[0049]

[Effect of the invention]

According to the present invention, the luminance of regions other than the aperture region, i.e., the luminance of regions alongside the drain lines, can be made uniform (nearly black) regardless of the rise and fall in the potential of the drain lines or the amount of space between the drain lines and common electrodes by: (1) making the direction of initial orientation of the liquid crystal in regions other than the aperture region orthogonal or parallel to the longitudinal direction of the pixel electrodes; (2) making the direction of initial orientation of the liquid crystal in regions other than the aperture region orthogonal to the substrate; and (3) providing a large potential difference between the light-shielding layer and signal electrode and making the direction of the liquid crystal in regions other than the aperture region orthogonal to

the substrate.

Display defects such as vertical crosstalk and stepping non-uniformity that were seen in liquid crystal displays of the prior art when viewed from an angle are thus not visible in a liquid crystal display according to the present invention, and the quality of display of the liquid crystal display is improved.

[Brief Explanation of the Drawing]

[Fig. 1]

A plan view showing the construction of one pixel in the liquid crystal display according to the first embodiment.

[Fig. 2]

A sectional view at line I-I of Fig. 1.

[Fig. 3]

A schematic plan view showing the partition of first alignment layer and second alignment layer.

[Fig. 4]

A schematic view showing the initial direction of alignment of liquid crystal molecules of an alignment layer in each partition shown in Fig. 3.

[Fig. 5]

A schematic view showing the initial direction of alignment of a liquid crystal of the first embodiment.

[Fig. 6]

A graph regarding a liquid crystal display according to the first embodiment and shows the relation between the electric field intensity between common electrodes and pixel electrodes and the optical transmittivity (%) when the amount of light emitted from the sheet polarizer on the side of the first glass substrate is 100.

[Fig. 7]

Fig. 7(a)- Fig. 7(c) is a schematic perspective view showing the each step in the process of fabricating a liquid crystal display 50.

[Fig. 8]

Fig. 8(a) and Fig. 8(b) is a schematic perspective view showing the each step in the process of fabricating a liquid crystal display 50.

[Fig. 9]

A plan view showing the construction of one pixel of the liquid crystal display according to the second embodiment.

[Fig. 10]

A sectional view at line II-II of the liquid crystal display shown in Fig. 9.

[Fig. 11]

A schematic view showing the initial direction of

alignment of the liquid crystal of the second embodiment.

[Fig. 12]

A graph regarding a liquid crystal display according to the second embodiment and shows the relation between the electric field intensity between common electrodes and pixel electrodes and the optical transmittivity (%) when the amount of light emitted from the sheet polarizer on the side of the first glass substrate is 100.

[Fig. 13]

Fig. 13(a)- Fig. 13(d) is a schematic perspective view showing the each step in the fabrication process of the liquid crystal display 60.

[Fig. 14]

Fig. 14(a)- Fig. 14(c) is a schematic perspective view showing the each step of another fabrication process of liquid crystal display 50.

[Fig. 15]

A plan view showing the construction of one pixel of a liquid crystal display according to the third embodiment.

[Fig. 16]

A sectional view at line III-III of the liquid crystal display shown in Fig. 15.

[Fig. 17]

An overall schematic view of a pixel unit.

[Fig. 18]

A partial sectional view of Fig. 17.

[Fig. 19]

An electric potential chart of the liquid crystal layer below the black matrix when displaying black in the third embodiment.

[Fig. 20]

An electric potential chart of the liquid crystal layer below the black matrix when displaying white in the third embodiment.

[Fig. 21]

An electric potential chart of the liquid crystal layer below the black matrix when displaying white in the prior art.

[Fig. 22]

A graph regarding a liquid crystal display according to an embodiment of the third embodiment and shows the relation between the difference in electric potential between common electrodes and pixel electrodes and the optical transmittivity (%) when the amount of light emitted from the sheet polarizer on the side of the first glass substrate is 100.

[Fig. 23]

A graph regarding a liquid crystal display according to the prior art and shows the relation between the difference in electric potential between common electrodes and pixel electrodes and the optical transmittivity (%) when the amount of light emitted from the sheet polarizer on the side of the first glass substrate is 100.

[Fig. 24]

A sectional view of an example of application of a substrate.

[Fig. 25]

A plan view showing the construction of one pixel of a liquid crystal display of the prior art.

[Fig. 26]

A sectional view taken at line I-I of the liquid crystal display shown in Fig. 1 and a sectional view taken at line V-V of the liquid crystal display shown in Fig. 25.

[Fig. 27]

Fig. 27(a) and Fig. 27(b) is a schematic view showing a display defect.

[Fig. 28]

A sectional view of a pixel for illustrating the cause

of the display defect shown in Fig. 27.

[Fig. 29]

Fig. 29(a) and Fig. 29(b) is a schematic view for illustrating the problem of the prior-art method for preventing display defects.

[Fig. 30]

Fig. 30(a) and Fig. 30(b) is a schematic view for illustrating the problem of another prior-art method for preventing display defects.

[Explanation of symbols]

- 10 prior-art IPS liquid crystal
- 12 first glass substrate
- 14 second glass substrate
- 16 common electrode
- 18 first insulating layer
- 20 drain line (signal line electrode)
- 22 pixel electrode
- 24 common line
- 26 second insulating layer
- 28 first alignment layer
- 30 scan line
- 32 thin film transistor
- 34 aperture
- 36 black matrix
- 38 color layer (color filter)

40 planarization film
42 second alignment layer
44 liquid crystal component layer
50 liquid crystal display of first embodiment
51 first alignment layer
52 aperture region
53 second alignment layer
54 region except aperture region
56 photo resist film
60 liquid crystal display of second embodiment
62 first alignment layer
64 second alignment layer
66 horizontal alignment layer
68 vertical alignment layer
70 photo resist film
72 pattern
74 photo-mask
75 photo resist film
76 pattern
77 photo-mask
78 printing plate
80 liquid crystal display of third embodiment
82 black matrix
84 silver paste
86 terminal
88 TCP

90 seal

92 color filter